



A10 Analog Processor
cPCI Module

Key Features

- 32-bit floating point Digital Signal Processor
- 16-bit A/D converter
- 4Mb of RAM
- Internal temperature monitor
- CompactPCI™ bus

Capable of making electrical measurements on 14 channels every ¼ cycle.

Samples and stores analog values from a chassis-mounted signal input module. Uses track and hold circuitry for processing through an A/D converter which ensures minimum phase errors.

Simultaneous sampling of all input signals allows the analog signals to be combined to form true Phase-to-Phase and Phase-to-Neutral differential measurements with extremely low phase errors. It also allows accurate calculation of bus-to-bus differential voltages and angles, and accurate power measurements.

The Digital Signal Processor (DSP) reads A/D converter outputs, calculates measurements, temporarily stores measurements, and services requests for measurements from the Host Processor Module through the cPCI backplane.

Has integral sensor to read internal temperature.

Features

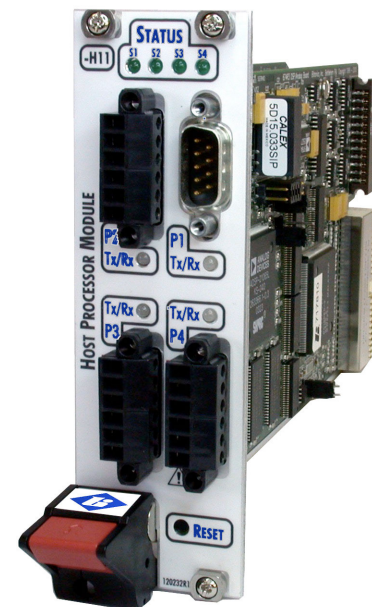
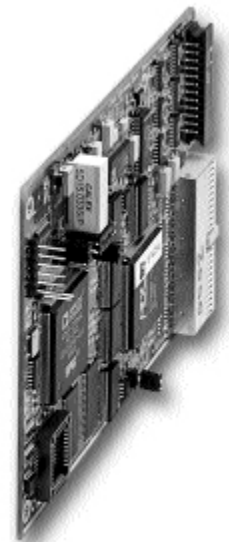
- Analog values are read form signal input module through a 24-pin header on backplane
- Track-and-hold circuitry
- 32-bit floating point DSP
- 16-bit A/D converter
- 128 samples per cycle on each of 14 channels
- Offset and gain values stored in non-volatile memory
- 4Mb RAM
- Designed to mount to the Host Processor Module

Module Chassis Interface

- One cPCI connector, 2 mm, hard metric for interface to other modules in chassis
- Complies with CompactCPI version 2.0, supporting 32 bit and speeds to 33MHZ
- One custom 24 pin analog signal connector Burden >7 Mohms differential and to ground

Module Chassis Interface

- -40C to +70C operating temperature



Analog Processor Module shown mounted to Host Processor Module

Contact: